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the present work completely integrates them. It is indeed a design-oriented approach. VHDL stands for VHSIC Hardware Description Language.

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Object- oriented reuse methodology for vhdl

In contrast to conventional reuse approaches, which imply a considerable re-design effort, Object-Oriented Reuse Methodology for VHDL

Semantics of a verification- oriented subset of

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(object-oriented Prolog extension) Machine languages are directly executable by a computer's CPU. Western Design Center 65816/65802 (CPU for Apple IIGS

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