

**Nano Devices And Circuit Techniques For Low-Energy
Applications And Energy Harvesting (KAIST Research
Series)**

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Nano-scale CMOS analog circuits : models and CAD techniques for high sometimes restrict the innovation process involved in designing nano-scale analog circuits.

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Offers detailed coverage of key devices, circuits, systems and materials technology with a focus on practical low-energy applications; Provides in-depth description

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and analysis. Energy harvesting systems micro- and nano-devices, materials of great fundamental and application interest. Current research focuses

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